

Attorney Docket No. P123-US

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of: Peter W. Richards

Art Unit: Not Yet Assigned

Serial No.: Not Yet Assigned

Examiner: Not Yet Assigned

Filed: Herewith

For: **AN INTERGRATED DRIVER FOR USE IN DISPLAY SYSTEMS HAVING  
MICROMIRRORS**

**INFORMATION DISCLOSURE STATEMENT**  
**PURSUANT TO 37 CFR 1.97(b)**

Commissioner for Patents  
Alexandria, VA 22313-145

Sir:

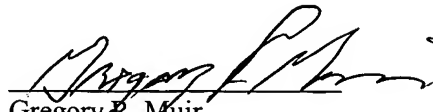
The attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached PTO Form 1449. Copies of the cited references are enclosed.

No fee or certification is required in connection with this Information Disclosure Statement, since it is being submitted prior to the last of (1) issuance of a first official action on the merits and (2) expiration of the three month period following filing of the above-captioned application.

The above information is presented so that the Patent and Trademark Office can determine any materiality thereof to the claimed invention. It is respectfully requested that the information be considered during the prosecution of this application and that the cited documents be listed on the front page of any patent issuing from this application.

The Patent Office is authorized to charge our Deposit Account No. 501516 for any fee which it deems to be required to effect consideration of this statement.

Respectfully submitted,

  
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<b>INFORMATION DISCLOSURE CITATION</b>  <b>PTO-1449</b>		ATTY. DOCKET NO. P123-US		SERIAL NO. Not Yet Assigned			
		APPLICANT Peter W. Richards					
		FILING DATE Herewith		GROUP Not Yet Assigned			
<b>U.S. PATENT DOCUMENTS</b>							
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE	
	US2002/0085438 A1	07/04/02	Wolverton				
	US2002/0138688 A1	09/26/02	Hsu, et al.				
	US2002/0054031 A1	05/09/02	Elliott, et al.				
	6,421,285	07/16/02	Matsuzaki, et al.				
	5,903,248	05/11/99	Irwin, et al.				
	4,638,309	01/20/87	Ott				
	6,344,672 B2	02/05/02	Huffman				
	5,132,928	07/21/92	Hayashikoshi, et al.				
	5,382,961	01/17/95	Gale, Jr.				
<b>FOREIGN PATENT DOCUMENTS</b>							
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
						<input type="checkbox"/>	<input type="checkbox"/>
						<input type="checkbox"/>	<input type="checkbox"/>
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)</b>							
	Kompenhouwer, et al., OPTIMALLY REDUCING MOTION ARTIFACTS IN PLASMA DISPLAYS, Phillips Research Laboratories, SID 00 Digest, Pgs 388-391.						
	Baker, et al., CMOS Circuit Design, Layout and Simulation, IEEE Press Series on Microelectronic Systems, IEEE Press, New York, Pgs 345-349.						

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<b>U.S. PATENT DOCUMENTS</b>							
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE	
	5,523,803	06/04/96	Urbanus, et al.				
	5,255,100	10/19/93	Urbanus				
	6,115,083	09/05/00	Doherty, et al.				
	6,107,979	08/22/00	Chiu, et al.				
	5,079,544	01/07/92	DeMond, et al.				
	5,548,301	08/20/96	Kornher, et al.				
	5,254,980	10/19/93	Hendrix, et al.				
	5,444,566	08/22/95	Gale, et al.				
<b>FOREIGN PATENT DOCUMENTS</b>							
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
						<input type="checkbox"/>	<input type="checkbox"/>
						<input type="checkbox"/>	<input type="checkbox"/>
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)</b>							
EXAMINER			DATE CONSIDERED				

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.